## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:

Christopher J. Diorio et al.

**SERIAL NO.:** 

Filed herewith

FILING DATE:

Filed Herewith

TITLE:

Rewriteable Electronic Fuses

**EXAMINER:** 

Unassigned

ART UNIT:

Unassigned

## **CERTIFICATE OF EXPRESS MAILING**

I hereby certify that this paper is being deposited with the United States Postal Service as Express Mail EV310857670US in an envelope addressed to: Mail Stop: New Patent Application, Commissioner for

Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date printed below:

Date: うろり

Name:

Beatrice Orozco

COMMISSIONER FOR PATENTS P.O. Box 1450 ALEXANDRIA, VA 22313-1450

## INFORMATION DISCLOSURE STATEMENT

Each item of information listed in the attached FORM PTO-1449, for which a copy of each is attached (unless the blanket waiver referred to below applies), may be material to the examination of the above-identified application and is, therefore, submitted in compliance with the duty of disclosure defined in 37 CFR §§ 1.56, 1.97 and 1.98. The Examiner is requested to review, consider and document each such item in the official record of this application.

| Note: If this | s box is checked, this case was filed after June 30, 2003 and qualifies for   |
|---------------|---|
| the blanket   | waiver of deposit of copies of U.S. Patents and U.S. Patent Application   |
| Publications  | s in accordance with the written waiver of 37 CFR §1.98 (a)(2)(i) dated July  |
| 11, 2003. A   | accordingly, such copies are not attached.  |
| This Inform   | ation Disclosure Statement under 37 CFR §§ 1.56, 1.97 and 1.98 is not to be   |
| construed as  | a representation that a search has been made, that additional information   |
| material to t | he examination of this application does not exist, or that any one or more of   |
| these items   | constitutes prior art.  |
|               | I   |
| This stateme  | ent is filed pursuant to (CHECK ONE BOX):   |
|               | 37 C.F.R. § 1.97(b).  This information disclosure statement is filed either:  (1) within three months of the filing date of a national application other than a continued prosecution application under §1.53(d);  (2) within three months of the date of entry of the national stage as set forth in 37 C.F.R. §1.491 in an international application;  (3) before the mailing date of a first office action on the merits; or  (4) before the mailing of a first office action after the filing of a Request for Continued Examination under 37 C.F.R. §1.114, whichever event occurs last. |
|               | Accordingly, this information disclosure statement requires no fee and no certification.  37 C.F.R. § 1.97(c).  This information disclosure statement is filed after the period specified in 37 C.F.R. § 1.97(b), but before the mailing date of any of the following:  (1) a final action under 37 C.F.R. § 1.113;  (2) a notice of allowance under 37 C.F.R. § 1.311; or  (3) an action that otherwise closes prosecution in the application.   |
|               | Accordingly, this information disclosure statement requires either:  (1) the fee specified in 37 C.F.R. § 1.17(p) for submission of an information disclosure statement under 37 C.F.R. § 1.97(c); or  (2) a certification according to 37 C.F.R. § 1.97 (e)(1) or (2).   |
|               | 37 C.F.R. § 1.97(d).  This information disclosure statement is filed after the period specified in 37 C.F.R. § 1.97 (c).  |

Accordingly, this information disclosure statement requires:

(1) a certification in accordance with 37 C.F.R. § 1.97(e); and

(2) the fee specified in 37 C.F.R. § 1.17 (p) to consider an information disclosure statement under 37 C.F.R. § 1.97(d).

If this statement crosses in the mail with an office action, or is otherwise not in the indicated category of 37 C.F.R. § 1.97, it is respectfully requested that this statement be treated in the next appropriate category and made of record. To the extent required, please treat this paper as a conditional petition for acceptance of the information disclosure statement.

II

| Fees Du  | ue (CI      | HECK ONE BOX):   |
|----------|-------------|--|
|          | $\boxtimes$ | No fee is due.   |
| [        |             | The fee specified in 37 C.F.R. § 1.17(p) for submission of an information disclosure statement under 37 C.F.R. § 1.97(c) or 37 C.F.R. § 1.97(d) is enclosed (\$180).   |
|          |             | III  |
| Certific | ation       | (CHECK ONE BOX):   |
|          | $\boxtimes$ | No certification is necessary.   |
| [        |             | Pursuant to 37 C.F.R. § 1.97(e)(1), the undersigned hereby certifies:  That each item of information contained in this information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement.  |
| [        |             | Pursuant to 37 C.F.R. § 1.97(e)(2), the undersigned hereby certifies:  No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned after making reasonable inquiry, no item of information contained in this information disclosure statement was known to any individual designated in 37 C.F.R. §1.56(c) more than three months prior to the filing of this information disclosure statement. |

IV

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Please charge any additional required fee or credit any overpayment to our deposit account number 50-1698.

Respectfully submitted, THELEN REID & PRIEST LLP

Dated: March 30, 2004

David B. Ritchie

Reg. No. 31,562

THELEN REID & PRIEST LLP P.O. Box 640640 San Jose, CA 95164-0640 (408) 292-5800 direct dial (408) 287-8040 direct fax

| Patent and Trademark Office  |         |     |              |                 |                        | ocket No.                               |                                  | Serial N  |        |             |            |  |
|--|---------|-----|--------------|-----------------|------------------------|---|----------------------------------|-----------|--------|-------------|------------|--|
| Information Disclosure Statement by Applicant (Use several sheets if necessary)  |         |     |              |                 |                        | IMPJ-00                                 | Unassigned Unassigned            |           |        |             |            |  |
| Class   Subclass   Filing Date   Name   Class   Subclass   Filing Date   | Applica |     |              |                 |                        | Applicat                                | nt: Christopher J. Diorio et al. |           |        |             |            |  |
| Init.  | Filed:  |     |              |                 |                        | Filed: H                                | Herewith Group: Unassigned       |           |        |             |            |  |
| Init.  |         |     |              |                 | U.S. Patent Docume     | nts                                     |                                  |           |        |             |            |  |
| B   5,825,063   10/20/1998   Diorio et al.   257   316   7/26/1996     C   5,875,126   2/23/1999   Minch et al.   365   185.01   9/26/1996     D   5,888,613   4/27/1999   Diorio et al.   365   185.03   6/25/1997     E   5,914,894   6/22/1999   Diorio et al.   365   185.03   6/1/1998     F   5,986,927   11/16/1999   Minch et al.   365   185.01   11/10/1998     G   5,990,512   11/23/1999   Diorio et al.   257   314   4/22/1997     H   6,125,053   9/26/2000   Diorio et al.   257   314   4/22/1997     H   6,125,053   9/26/2000   Diorio et al.   365   185.03   11/30/1998     J   6,222,765   4/24/2001   Nojima   365   185.03   11/30/1998     J   6,222,765   4/24/2001   Nojima   365   185.08   2/18/2000     K   6,452,835   9/17/2002   Diorio et al.   365   185.08   2/18/2000     L   6,477,103   11/5/2002   Nojima   365   185.03   10/27/2000     L   6,477,103   11/5/2002   Nojima   365   185.03   10/27/2000     M   6,661,278   12/9/2003   Gilliland   327   536   7/8/2002     N   6,664,909   12/16/2003   Hyde et al.   341   144   8/13/2001     Foreign Documents   Foreign Documents   Translation     Translation     | Init.   |     |              |                 |                        |   |                                  | ubclass   | Filing | Filing Date |            |  |
| C   5,875,126   2/23/1999   Minch et al.   365   185.01   9/26/1996     D   5,898,613   4/27/1999   Diorio et al.   365   185.03   6/25/1997     E   5,914,894   6/22/1999   Diorio et al.   365   185.03   6/1/1998     F   5,986,927   11/16/1999   Minch et al.   365   185.01   11/10/1998     G   5,990,512   11/23/1999   Diorio et al.   257   314   4/22/1997     H   6,125,053   9/26/2000   Diorio et al.   365   185.03   11/30/1998     I   6,144,581   11/7/2000   Diorio et al.   365   185.03   11/30/1998     J   6,222,765   4/24/2001   Nojima   365   185.03   11/30/1998     J   6,222,765   4/24/2001   Nojima   365   185.03   11/30/1998     L   6,477,103   11/5/2002   Diorio et al.   365   185.03   10/27/2000     L   6,477,103   11/5/2002   Nguyen et al.   365   225.7   9/21/2001     M   6,661,278   12/9/2003   Gilliland   327   536   7/8/2002     N   6,664,909   12/16/2003   Hyde et al.   341   144   8/13/2001     Foreign Documents   Foreign Documents   Translation     Init.   Document No.   Date   Country   Class   Subclass   Yes   No     O   Raszka, Jaroslav, et al., "Embedded Flash Memory for Security Applications in a 0.13µm CMOS Logic     Process", IEEE 2004 International Solid-State Circuits Conference, February 16, 2004, pp. 46-47.     Examiner   Date Considered   Date Considered   Date Considered   Country    |         | Α   | 5,761,121    | 6/2/1998        | Chang                  | 1.0                                     | 365                              |           | 85.14  | 10/31/      | 10/31/1996 |  |
| D   5,898,613   4/27/1999   Diorio et al.   365   185.03   6/25/1997     E   5,914,894   6/22/1999   Diorio et al.   365   185.03   6/1/1998     F   5,986,927   11/16/1999   Minch et al.   365   185.01   11/10/1998     G   5,990,512   11/23/1999   Diorio et al.   257   314   4/22/1997     H   6,125,053   9/26/2000   Diorio et al.   365   185.03   11/30/1998     I   6,144,581   11/7/2000   Diorio et al.   365   185.03   11/30/1998     J   6,222,765   4/24/2001   Nojima   365   185.03   11/30/1998     J   6,222,765   4/24/2001   Nojima   365   185.03   10/27/2000     K   6,452,835   9/17/2002   Diorio et al.   365   185.03   10/27/2000     L   6,477,103   11/5/2002   Nguyen et al.   365   225.7   9/21/2001     M   6,661,278   12/9/2003   Gilliland   327   536   7/8/2002     N   6,664,909   12/16/2003   Hyde et al.   341   144   8/13/2001     Foreign Documents   Foreign Documents     Foreign Documents   Class   Subclass   Yes   No     O   Raszka, Jaroslav, et al., "Embedded Flash Memory for Security Applications in a 0.13µm CMOS Logic   Process", IEEE 2004 International Solid-State Circuits Conference, February 16, 2004, pp. 46-47.     Examiner   Date Considered   Date Considered   Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if   |         | В   | 5,825,063    | 10/20/1998      | Diorio et al.          |   | 257                              | 31        | 16     | 7/26/1      | 7/26/1996  |  |
| E   5,914,894   6/22/1999   Diorio et al.   365   185.03   6/1/1998     F   5,986,927   11/16/1999   Minch et al.   365   185.01   11/10/1998     G   5,990,512   11/23/1999   Diorio et al.   257   314   4/22/1997     H   6,125,053   9/26/2000   Diorio et al.   365   185.03   11/30/1998     I   6,144,581   11/7/2000   Diorio et al.   365   185.03   11/30/1998     J   6,222,765   4/24/2001   Nojima   365   185.08   2/18/2000     K   6,452,835   9/17/2002   Diorio et al.   365   185.03   10/27/2000     L   6,477,103   11/5/2002   Nguyen et al.   365   225.7   9/21/2001     M   6,661,278   12/9/2003   Gilliland   327   536   7/8/2002     N   6,664,909   12/16/2003   Hyde et al.   341   144   8/13/2001     Foreign Documents   Foreign Documents     Translation     Init.   Document No.   Date   Country   Class   Subclass   Yes   No     O   Raszka, Jaroslav, et al., "Embedded Flash Memory for Security Applications in a 0.13μm CMOS Logic     Process", IEEE 2004 International Solid-State Circuits Conference, February 16, 2004, pp. 46-47.     Examiner   Date Considered     Examiner:   Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if  |         | С   | 5,875,126    | 2/23/1999       | Minch et al.           |   | 365                              | 18        | 85.01  | 9/26/1      | 9/26/1996  |  |
| F   5,986,927   11/16/1999   Minch et al.   365   185.01   11/10/1998  |         | D   | 5,898,613    | 4/27/1999       | Diorio et al.          | , , , , , ,                             | 365                              | 18        | 85.03  | 6/25/1      | 6/25/1997  |  |
| G   5,990,512   11/23/1999   Diorio et al.   257   314   4/22/1997     H   6,125,053   9/26/2000   Diorio et al.   365   185.03   11/30/1998     I   6,144,581   11/7/2000   Diorio et al.   365   185.03   11/30/1998     J   6,222,765   4/24/2001   Nojima   365   185.08   2/18/2000     K   6,452,835   9/17/2002   Diorio et al.   365   185.03   10/27/2000     L   6,477,103   11/5/2002   Nguyen et al.   365   225.7   9/21/2001     M   6,661,278   12/9/2003   Gilliland   327   536   7/8/2002     N   6,664,909   12/16/2003   Hyde et al.   341   144   8/13/2001     Foreign Documents   |         | Е   | 5,914,894    | 6/22/1999       | Diorio et al.          |   | 365                              | 18        | 85.03  | 6/1/19      | 6/1/1998   |  |
| H   6,125,053   9/26/2000   Diorio et al.   365   185.03   11/30/1998     I   6,144,581   11/7/2000   Diorio et al.   365   185.03   11/30/1998     J   6,222,765   4/24/2001   Nojima   365   185.08   2/18/2000     K   6,452,835   9/17/2002   Diorio et al.   365   185.03   10/27/2000     L   6,477,103   11/5/2002   Nguyen et al.   365   225.7   9/21/2001     M   6,661,278   12/9/2003   Gilliland   327   536   7/8/2002     N   6,664,909   12/16/2003   Hyde et al.   341   144   8/13/2001     Foreign Documents   Translation     Init.   Document No.   Date   Country   Class   Subclass   Yes   No     O   Raszka, Jaroslav, et al., "Embedded Flash Memory for Security Applications in a 0.13µm CMOS Logic Process", IEEE 2004 International Solid-State Circuits Conference, February 16, 2004, pp. 46-47.    Examiner   Date Considered   Date Considered     Date Considered   Examiner   Examiner   Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if  |         | F   | 5,986,927    | 11/16/1999      | Minch et al.           |   | 365                              | 18        | 85.01  | 11/10/      | 11/10/1998 |  |
| I   6,144,581   1177/2000   Diorio et al.   365   185.03   11730/1998     J   6,222,765   4/24/2001   Nojima   365   185.08   2/18/2000     K   6,452,835   9/17/2002   Diorio et al.   365   185.03   10/27/2000     L   6,477,103   11/5/2002   Nguyen et al.   365   225.7   9/21/2001     M   6,661,278   12/9/2003   Gilliland   327   536   7/8/2002     N   6,664,909   12/16/2003   Hyde et al.   341   144   8/13/2001     Foreign Documents   Foreign Documents  |         | G   | 5,990,512    | 11/23/1999      | Diorio et al.          |   | 257                              | 31        | 14     | 4/22/1      | 4/22/1997  |  |
| J   6,222,765   4/24/2001   Nojima   365   185.08   2/18/2000     K   6,452,835   9/17/2002   Diorio et al.   365   185.03   10/27/2000     L   6,477,103   11/5/2002   Nguyen et al.   365   225.7   9/21/2001     M   6,661,278   12/9/2003   Gilliland   327   536   7/8/2002     N   6,664,909   12/16/2003   Hyde et al.   341   144   8/13/2001     Foreign Documents   Foreign Documents  |         | Н   | 6,125,053    | 9/26/2000       | Diorio et al.          | *************************************** | 365                              | 18        | 85.03  | 11/30/      | 11/30/1998 |  |
| K   6,452,835   9/17/2002   Diorio et al.   365   185.03   10/27/2000     L   6,477,103   11/5/2002   Nguyen et al.   365   225.7   9/21/2001     M   6,661,278   12/9/2003   Gilliand   327   536   7/8/2002     N   6,664,909   12/16/2003   Hyde et al.   341   144   8/13/2001     Foreign Documents   |         | I   | 6,144,581    | 11/7/2000       | Diorio et al.          | 7240                                    | 365                              | 18        | 85.03  | 11/30/      | 11/30/1998 |  |
| L  |         | J   | 6,222,765    | 4/24/2001       | Nojima                 |   | 365                              | 18        | 85.08  | 2/18/2      | 2/18/2000  |  |
| M 6,661,278 12/9/2003 Gilliland 327 536 7/8/2002 N 6,664,909 12/16/2003 Hyde et al. 341 144 8/13/2001    Foreign Documents   Translation   |         | K   | 6,452,835    | 9/17/2002       | Diorio et al.          | <del></del>                             | 365                              | 18        | 85.03  | 10/27/      | 10/27/2000 |  |
| N   6,664,909   12/16/2003   Hyde et al.   341   144   8/13/2001   |         | L   | 6,477,103    | 11/5/2002       | Nguyen et al.          |   | 365                              | 22        | 25.7   | 9/21/2      | 9/21/2001  |  |
| Foreign Documents  Translation  Init. Document No. Date Country Class Subclass Yes No  Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)  O Raszka, Jaroslav, et al., "Embedded Flash Memory for Security Applications in a 0.13µm CMOS Logic Process", IEEE 2004 International Solid-State Circuits Conference, February 16, 2004, pp. 46-47.  Examiner  Date Considered  Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if   |         | M   | 6,661,278    | 12/9/2003       | Gilliland              | 327                                     |                                  | 536       |        | 7/8/20      | 7/8/2002   |  |
| Init.   Document No.   Date   Country   Class   Subclass   Yes   No  |         | N   | 6,664,909    | 12/16/2003      | Hyde et al.            |   | 341 144                          |           | 14     | 8/13/2      | 8/13/2001  |  |
| Init.   Document No.   Date   Country   Class   Subclass   Yes   No  | •       |     | ·            |                 | Foreign Document       | s                                       |                                  | •         |        | •           |            |  |
| Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)  O Raszka, Jaroslav, et al., "Embedded Flash Memory for Security Applications in a 0.13µm CMOS Logic Process", IEEE 2004 International Solid-State Circuits Conference, February 16, 2004, pp. 46-47.  Examiner  Date Considered  Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if  |         |     |              |                 |                        |   |                                  |           |        |             | nslation   |  |
| O Raszka, Jaroslav, et al., "Embedded Flash Memory for Security Applications in a 0.13µm CMOS Logic Process", IEEE 2004 International Solid-State Circuits Conference, February 16, 2004, pp. 46-47.  Examiner Date Considered  Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if   | Init.   |     | Document No. | Date            | Country                |   | Class                            | Subclass  |        | Yes         | No         |  |
| O Raszka, Jaroslav, et al., "Embedded Flash Memory for Security Applications in a 0.13µm CMOS Logic Process", IEEE 2004 International Solid-State Circuits Conference, February 16, 2004, pp. 46-47.  Examiner Date Considered  Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if   |         |     |              |                 |                        |   |                                  |           |        |             |            |  |
| O Raszka, Jaroslav, et al., "Embedded Flash Memory for Security Applications in a 0.13µm CMOS Logic Process", IEEE 2004 International Solid-State Circuits Conference, February 16, 2004, pp. 46-47.  Examiner Date Considered  Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if   |         |     |              |                 |                        |   |                                  |           |        |             |            |  |
| O Raszka, Jaroslav, et al., "Embedded Flash Memory for Security Applications in a 0.13µm CMOS Logic Process", IEEE 2004 International Solid-State Circuits Conference, February 16, 2004, pp. 46-47.  Examiner Date Considered  Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if   |         |     |              |                 |                        |   |                                  |           |        |             |            |  |
| Process", IEEE 2004 International Solid-State Circuits Conference, February 16, 2004, pp. 46-47.    Date Considered   Da |         |     | Other Do     | ocuments (Inclu | iding Author, Title, I | Date, Pert                              | inent Page                       | es, etc.) |        |             |            |  |
| Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if   |         |     |              |                 |                        |   |                                  |           |        |             |            |  |
| Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if   |         |     |              |                 |                        |   |                                  |           |        |             |            |  |
| Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if   |         |     |              |                 |                        |   | • •                              |           | _      |             |            |  |
| Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if   |         |     |              | W 1/50 ·        |                        |   |                                  |           |        |             |            |  |
| Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if   |         |     |              |                 |                        |   |                                  |           |        |             |            |  |
| Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if   |         |     |              |                 |                        |   |                                  |           |        |             |            |  |
| Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if   |         |     |              |                 |                        |   |                                  |           |        |             |            |  |
|  | Examir  | ner |              |                 |                        |   | Date Con                         | sidered   | I      |             |            |  |
| not in conformance and not considered. Include a copy of this form with the next communication to applicant.   |         |     |              |                 |                        |   |                                  |           |        | through cit | ation if   |  |

| Form PTO 1449 |                             |                           |                   |                       | Atty. Docket No.<br>IMPJ-0027C    |   |        | Serial No.: |            |             |  |
|---------------|-----------------------------|---------------------------|-------------------|-----------------------|-----------------------------------|---|--------|-------------|------------|-------------|--|
| (Rev. 2       | 2-32)                       | U.S. Departmen            |                   |                       | IMPJ-00                           | J27C                                    |        | Unassi      | Unassigned |             |  |
|               | Patent and Trademark Office |                           |                   |                       |                                   | Applicant: Christopher J. Diorio et al. |        |             |            |             |  |
|               | <u>In</u>                   | formation Disclosure      | Statement by      | Applicant             | Filed, Heavilth Court Heaviered   |   |        |             |            |             |  |
|               |                             | (Use several sh           | eets if necessary | y)                    | Filed: Herewith Group: Unassigned |   |        |             |            |             |  |
|               |                             |                           |                   | U.S. Patent Docume    | ents                              |   |        |             |            |             |  |
| Init.         |                             | Document No.              | Date              | Name                  |                                   |   |        | Subclass    | Filing     | Filing Date |  |
|               | P                           | 2003/0206437              | 11/6/2003         | Diorio et al.         |                                   | 365                                     |        | 185.03      | 7/9/20     | 002         |  |
|               | Q                           | 2004/0004861              | 1/8/2004          | Srinivas et al.       |                                   | 365                                     |        | 185.21      | 7/5/20     | 7/5/2002    |  |
|               | R                           | 2004/002/1166             | 2/5/2004          | Hyde et al.           |                                   | 257                                     |        | 314         | 1/31/2     | 1/31/2003   |  |
|               | S                           | 2004/0037127              | 2/26/2004         | Lindhorst et al.      | · ·                               |   | 365    |             | 5/12/2     | 5/12/2003   |  |
|               | T                           | 2004/0052113              | 3/18/2004         | Diorio et al.         |                                   | 365                                     |        | 185.21      | 9/16/      | 9/16/2002   |  |
|               |                             |                           |                   |                       |                                   |   |        |             |            |             |  |
|               |                             |                           |                   |                       |                                   |   |        |             |            |             |  |
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